



Technical specifications

OEM
8x32 UT board

M 2 M

Phased Array Technologies

www.bercli.net

8x32 UT board
for third-party OEM integration

Phased-array

- Customized focusing, electronic scanning, and sectorial scanning
- Inspection mode: pulse echo with electronic selection of active channels
- Fast multiplexing of focal laws during electronic scanning

Pulsers

- Adjustable voltage: 10 to 80V with 1V steps
- Negative rectangular pulse, adjustable width: 20 ns to 1.2 μ s, steps of 2.5 ns
- Fall time < 10ns (80V, 50 Ω)
- Maximum PRF: 2 kHz (on USB power supply) to 10kHz (external power supply)

Receivers

- Bandwidth: 0.8 to 20 MHz
- Adjustable gain on each channel from 0 to 80 dB
- Adjustable analog DAC 80 dB (max. 20 dB/ μ s)
- Crosstalk between two channels: gain > 50 dB
- Maximum input signal amplitude \pm 1V

Digitizer

- Maximum sampling frequency: 100 MHz (adjustable from 100 MHz to 6.6 MHz)
- Range: 8 bits
- Input impedance: 50 Ω
- Global delay: 0 up to 1.6 ms, steps of 10ns
- Delay laws at transmission/reception: 0 to 20 μ s, steps of 2.5 ns

Input-Output

- USB2 for power supply and data transfer
- 1 FRB (Hypertronix) for phased-array probe or SubD HD
- Encoders input
- External power supply input



Dimensions

- Length: 200mm (7.9")
- Width: 110mm (4.3")
- Thickness: 55mm (2.2")

Interface

- Dynamic link libraries (dlls) provided
- Connectors:
 - USB2 connector
 - Probe connector
- Optional: board delivered in case providing the listed connections

MULTI2000 MULTIX MULTIX LF POCKET